

ACTEL UNVEILS INDUSTRY'S FIRST FLASH-BASED FPGAS QUALIFIED TO FULL MILITARY SPECIFICATIONS

ProASIC^{PLUS} Solutions Now Available in High-Reliability Plastic and Hermetic Packaging, and Full MIL-STD-883B Screening

MOUNTAIN VIEW, Calif., August 25, 2003 - Representing the industry's first flash-based FPGAs qualified for military applications, Actel Corporation (Nasdaq: ACTL) today announced the availability of its reprogrammable, nonvolatile ProASIC Plus field-programmable gate arrays (FPGAs) tested and verified to operate over the full military temperature range (-55°C to +125°C). With densities ranging from 300,000 to 1-million system gates, the single-chip, military-qualified APA300, APA600 and APA1000 devices are offered in three packaging and screening options - military-temperature plastic (MTP), military-temperature hermetic (MTH), and hermetic packaging fully screened to MIL-STD 883 class B (883B). These packaging options, combined with the family's low power consumption, design security and firm-error immunity, make the ProASIC Plus solutions suitable for a wide range of military, aerospace and avionics applications, including ground-, air- and sea-based military systems, radar, command and control, and navigation systems.

"Designers of high-temperature, high-reliability systems are looking for a solution that meets their stringent requirements without the lengthy design time and high non-recurring engineering costs associated with ASICs," said Ken O'Neill, director of marketing, military and aerospace products at Actel. "The new flash-based, military ProASIC Plus devices directly address this need and offer designers a 'best-of-both-worlds' scenario - ASIC-like features, such as low power consumption, single chip and live at power-up, combined with in-field upgradability. Further, with this introduction of military packaging and screening options, we have reinforced our continuing commitment to develop high-reliability products for the military market."

About the ProASIC Plus Family

The ProASIC Plus family, Actel's second-generation of flash-based FPGAs, consists of seven devices ranging in density from 75,000 to 1-million system gates. The combination of a fine-grained, single-chip ASIC-like architecture and nonvolatile flash configuration memory makes Actel's ProASIC Plus offering a strong ASIC alternative. The devices are live at power up, low power, highly secure and require no separate configuration memory, all characteristics shared by ASICs. Key features of the ProASIC Plus family include multiple phase-locked loops (PLLs), support for up to

198k bits of two-port embedded SRAM and 712 user-configurable I/Os, and improved in-system programmability (ISP).